

BRCM24C32SC



DATA SHEET

BRCM24C32SC SOP-8 32 Kbit I²C

1.7V	2.5~5.5V	1Mhz	1.7~2.5V
400Khz			
CMOS	400uA	1.6mA	

/ Pinning

Pin	Name	Type	Description
1	E0	Input	
2	E1	Input	
3	E2	Input	
4	GND	Ground	
5	SDA	I/O	/
6	SCL	Input	
7	WCB	Input	
8	VCC	Power	

/ Marking

/ Absolute Maximum Ratings(Ta=25)

			mA
			V

/ Reliability Characteristic

		N ₁				

		≤	≤ ≤	
--	--	---	--------	--

/ Functional Description

p / Data Input

SDA

SDA

SCL

(1) SCL

SDA

(1)

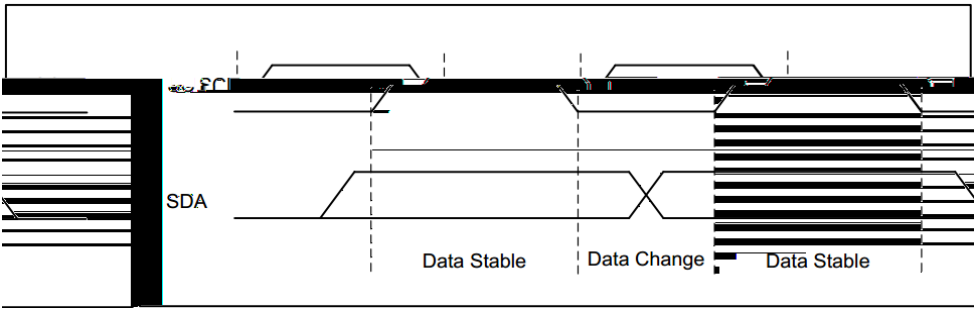


Figure 1 Data Validity

p / Start Condition

SCL

SDA

2

p / Stop Condition

SCL

SDA

BRCM24C32SC

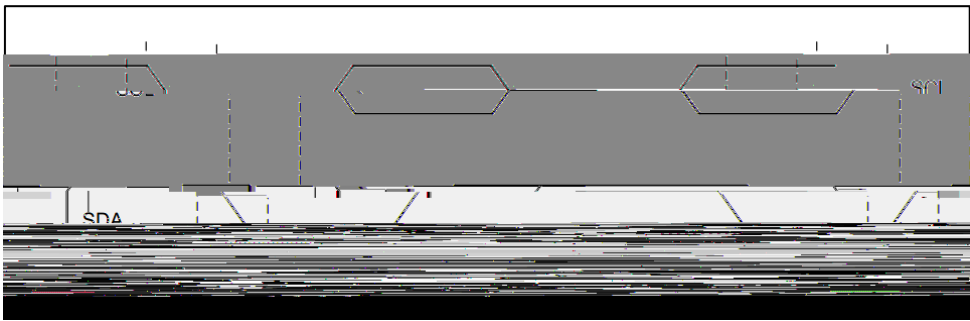


Figure 2 Start and Stop Definition

p ACK / Acknowledge

ACK

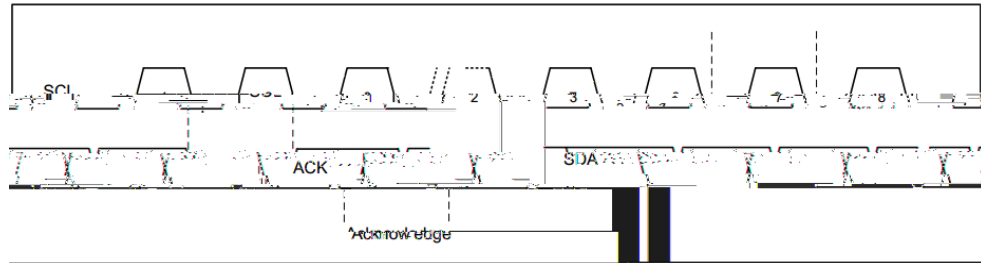
BRCM24C32SC

BRCM24C32SC

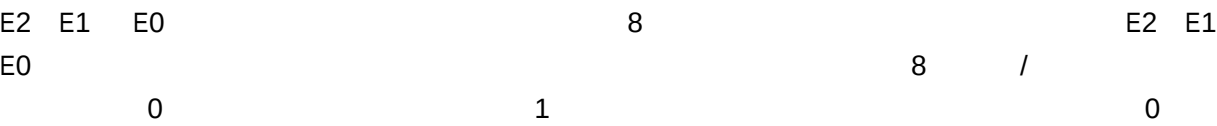
“ 0 ”

9

/ Functional Description



/ Functional Description



p / Data Security

BRCM24C32SC WCB Vcc

p / Byte Write



/ Functional Description

p / Acknowledge Polling

BRCM24C32SC

BRCM24C32SC



DATA SHEET

/ Functional Description

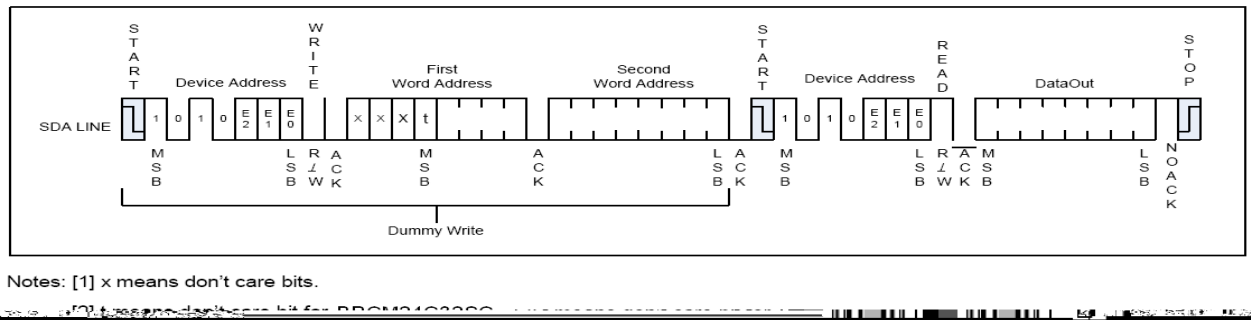


Figure 8 Random Address Read

p / Sequential Read

BRCM24C32SC

“ 0 ”

9

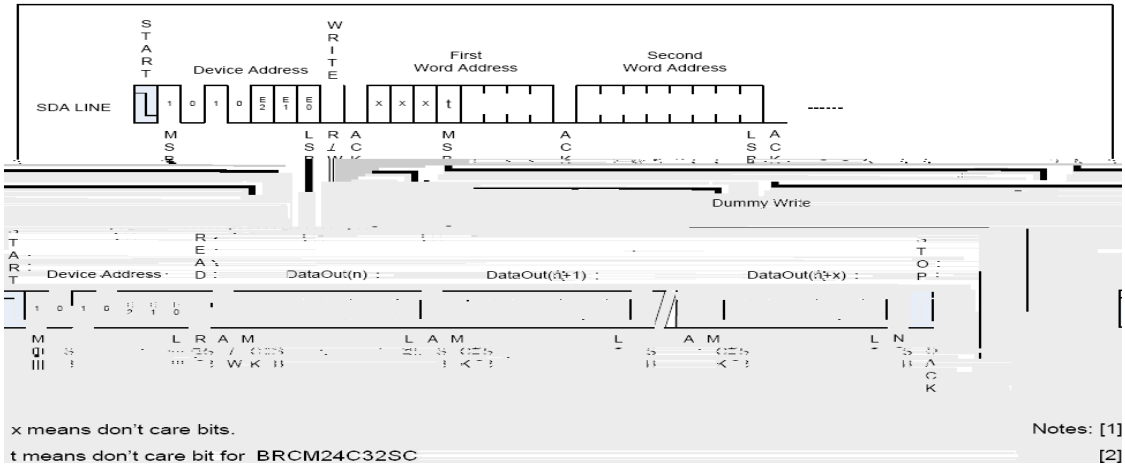
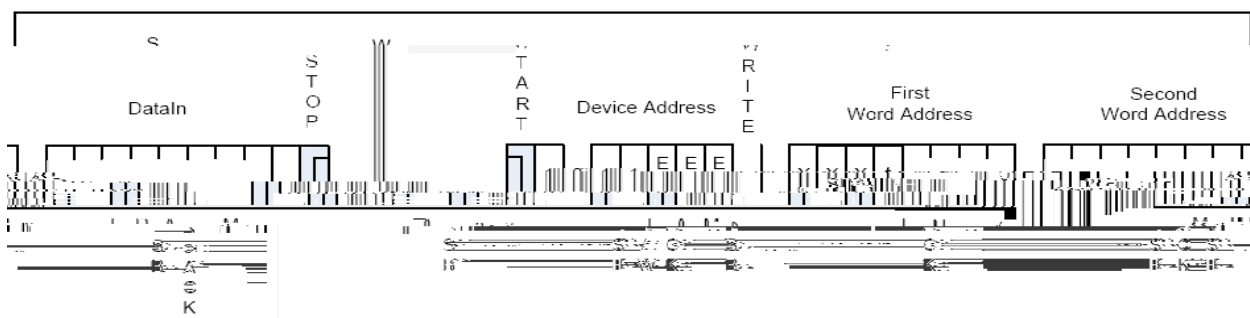


Figure 9

p / Read Identification Page

1011b	A15 / A5	A4 / A0	ID	
		10d	22	ID
32				

p / Read the Lock Status

$$\frac{[ACK] + [NoACK]}{10}$$


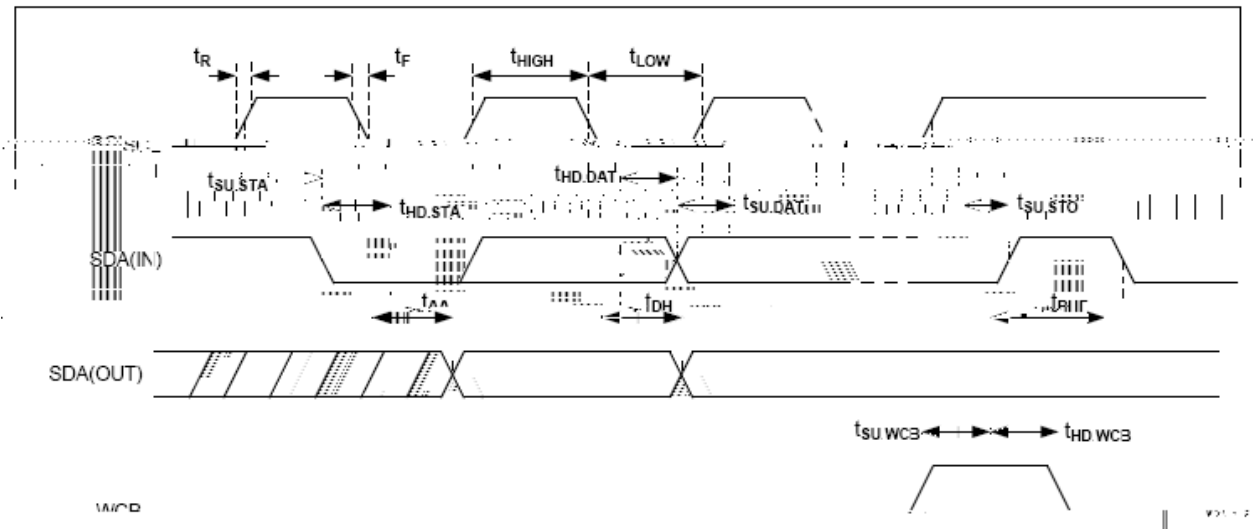
BRCM24C32SC



DATA SHEET

/ Time Sequence Diagram

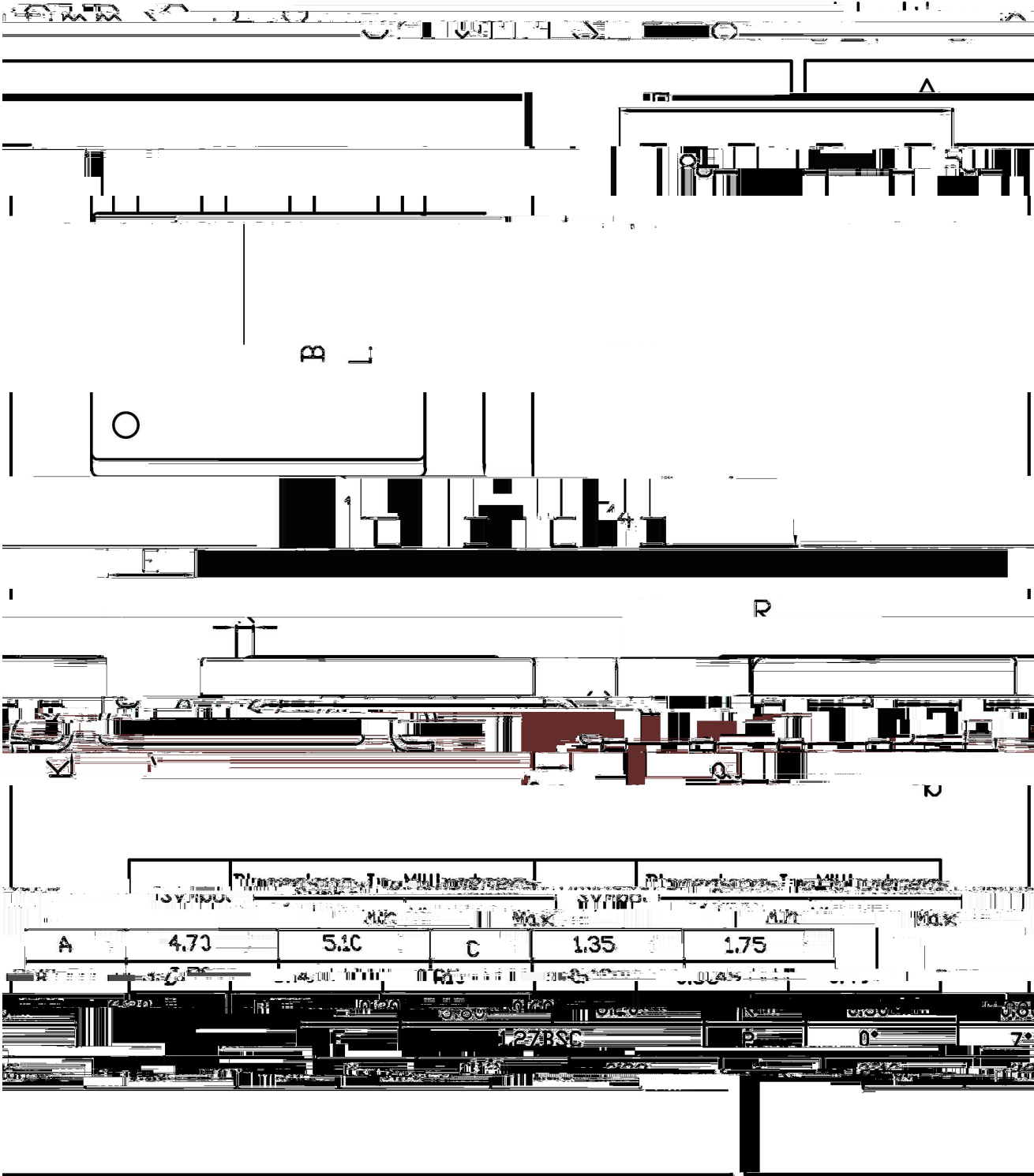
Bus Timing



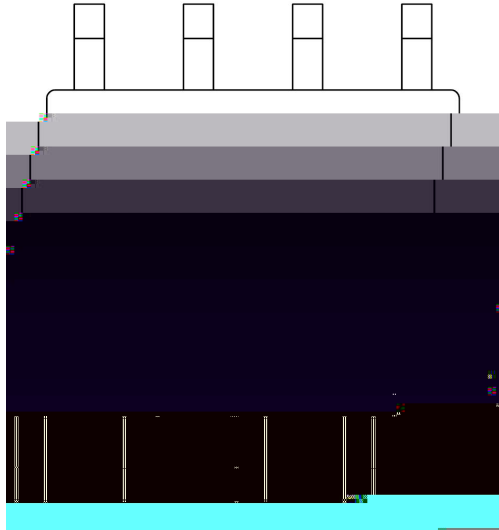
Write Cycle Timing



/ Package Dimensions



/ Marking Instructions



24C32

BRCM24C32SC