



/ Revised record

**/ Descriptions**

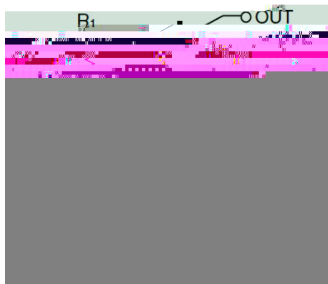
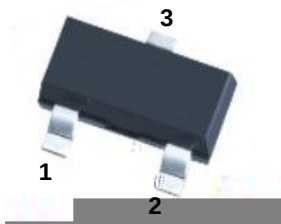
SOT-23 PNP Silicon PNP Digital transistor in a SOT-23 Plastic Package.

/ Features

With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process,HF Product.

/ Applications

Switching applications, interface circuit and driver circuit applications.

/ Equivalent Circuit**/ Pinning**

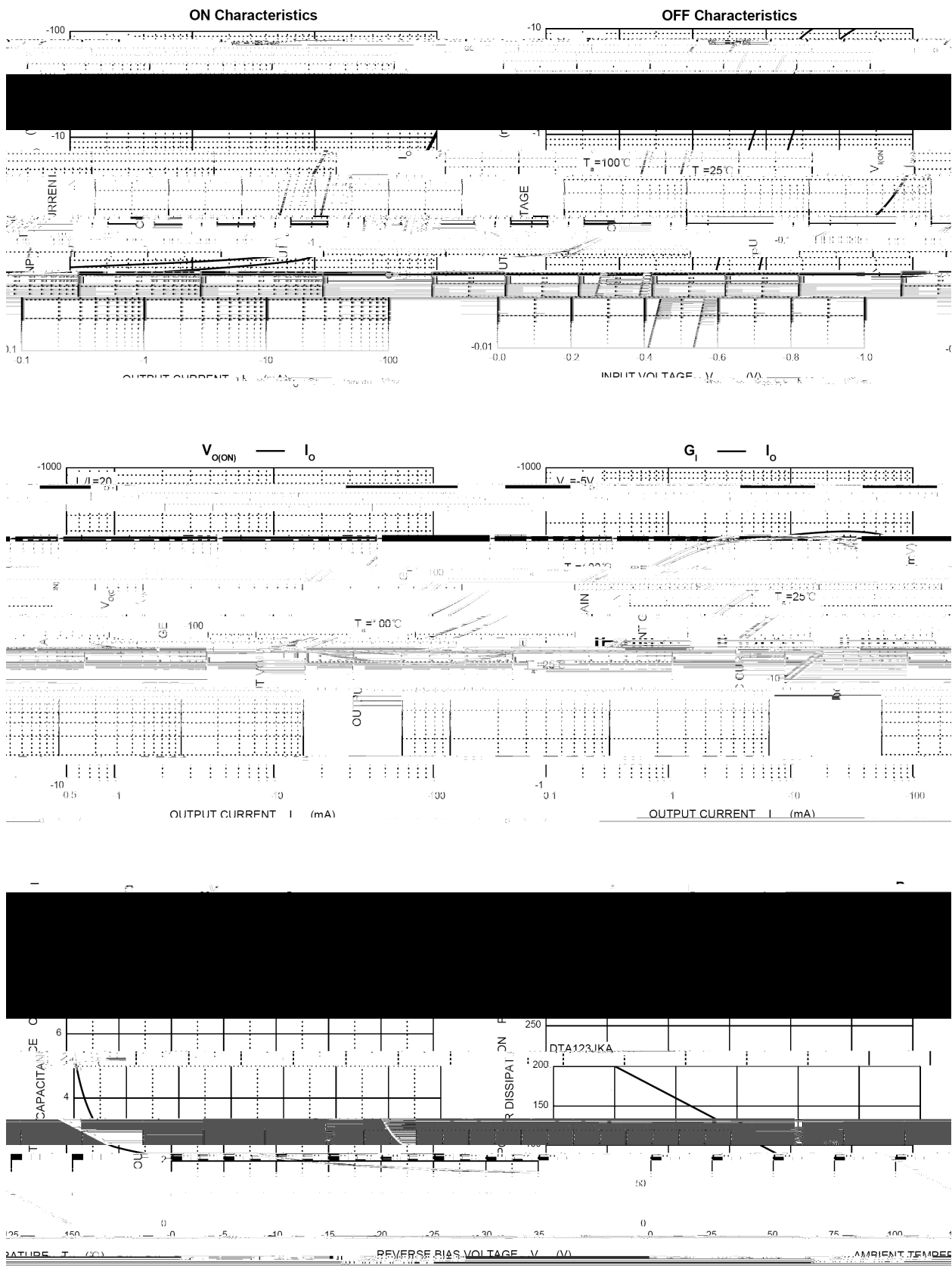
PIN 1 IN PIN 2 GND PIN 3 OUT

/ Marking

Marking	HE32
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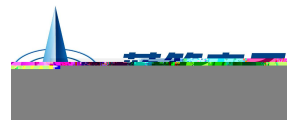
/ Electrical Characteristic Curve



DTA123JKA

Rev.A Apr.-2022

DATA SHEET



() /

Note:

1 150 180 60 90sec;

1.Preheating:150~180°C, Time:60~90sec.

2 0.5sec;

2.Peak Temp.:245±5°C, D

3

3. Cooling Speed: 2~10°C/

Soldering Heat Test Conditions

260±5°C

10±1 sec.

Temp.:260±5°C

Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
							盒	箱
						"		